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November 10, 2003

CUSTOMER NO.: 31561

Application No.: 10/064,426

Attorney Docket No.: 7983-US-PA

Page(s): Cover +1

FAX NO.: 002-1-703-872-9318

TO: UNITED STATES PATENT AND TRADEMARK OFFICE
Examiner: THAI, LUAN C
Art Unit: 2827

MESSAGE: Enclosed please find the Response to Restriction Requirement

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PatentCustomer No.: 31561
Application No.: 10/064,426
Docket No. 7983-US-PA**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Applicant : Chang, et al.
Application No. : 10/064,426
Filed : July 12, 2002
For : LAYOUT STRUCTURE AND METHOD FOR
SUPPORTING TWO DIFFERENT PACKAGE
TECHNIQUES OF CPU
Art Unit : 2827
Examiner : THAI, LUAN C

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RESPONSE TO RESTRICTION REQUIREMENTASSISTANT COMMISSIONER FOR PATENTS
Arlington, VA 22202**OFFICIAL**

Dear Sirs,

In response to the Office Action, mailed on October 8, 2003, Applicant would like to elect Group I, claims 1-28, drawn to a semiconductor device, classified in class 257, subclass 691. Please cancel Group II, claims 29-39, drawn to a method of making a semiconductor device, classified in class 438, subclass 106+ without waiver, prejudice or disclaimer.

Thank you for your assistance in the subject matter. If you have any questions, please feel free to contact me.

Respectfully Submitted,
JIANQ CHYUN IP OfficeDate: Nov 10, 2003By: Belinda Lee
Registration No.: 46,863

Please send future correspondence to:
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